

2001

Upgrade Plan for HANARO Control Computer System

, , , , ,

150

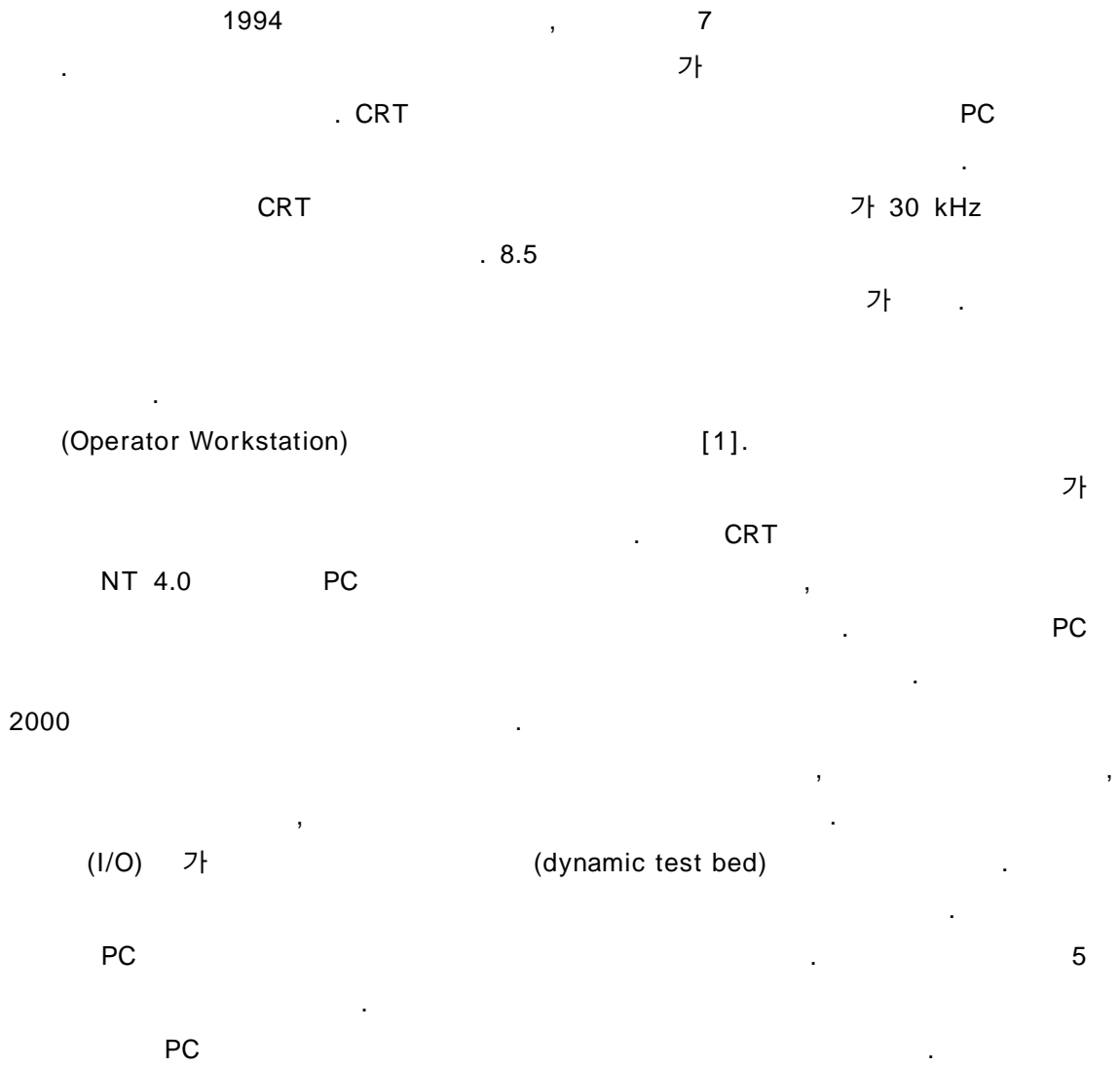
(Multi-Loop Controller) 80

가 .

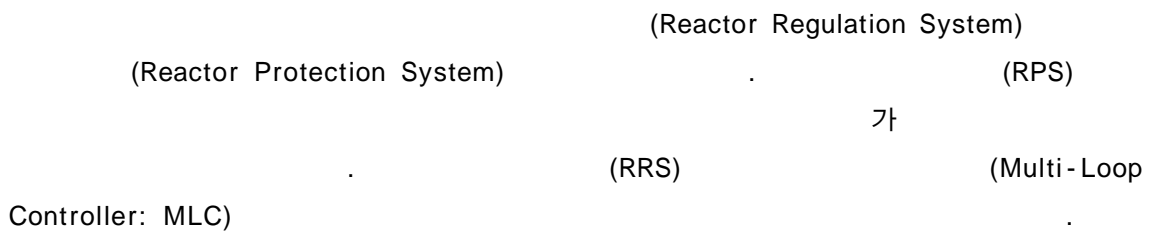
Abstract

A microprocessor based digital control system, the Multi-Loop Controller (MLC), which was chosen to control HANARO, was introduced to the market in early '80s and it had been used to control petrochemical plant, paper mill and Slowpoke reactor in Canada. Due to the development in computer technology, it has become so outdated model and the production of this model was discontinued a few years ago. Hence difficulty in acquiring the spare parts is expected. To achieve stable reactor control during its lifetime and to avoid possible technical dependency to the manufacturer, a long-term replacement plan for HANARO control computer system is on its way. The plan will include a few steps in its process. This paper briefly introduces the methods of implementation of the process and discusses the engineering activities of the plan.

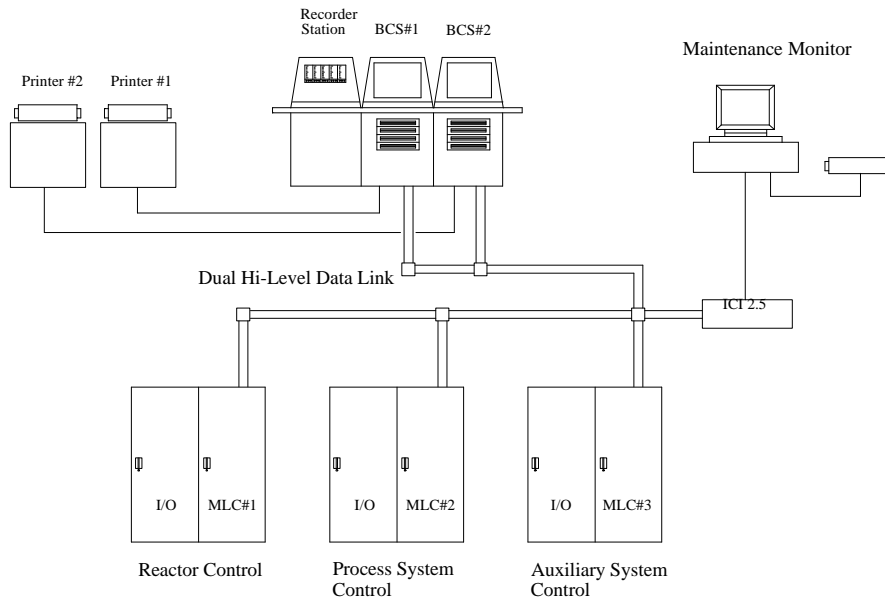
1.



2.



Moore MLC가
 3 MLC MLC#1
 , MLC#2 , MLC#3
 [2]. MLC 1



1.

BCS(Basic CRT Station) MLC
 가 (fault-tolerant)
 . MLC 가 2 , 가
 가
 . MLC
 가
 MLC 30
 .
 . 128
 BCS (Human Interface)

MLC . BCS 19 , ,
(multi-processing microcomputer center) 2 8.5
5 3 .

3.

MLC . 1994 2
MLC 가
1 14 10 BCS
,
가 .
BCS MLC 가 .
MLC
가 .
,
BCS .
MLC PC .

1.

1	96. 11	MLC#2 Rack A 5V DC Rack B	.
2	97. 3	CRT BCS#1	
3	97. 7	BCS#2 ()	
4	97. 8	MLC#1 Rack B 5V DC (Rack A)	
5	97. 11	BCS#1 (가 가)	
6	97. 11	D/A	()
7	98. 3	BCS#1 (CRT)	PC monitor 가 가 가
8	98. 5	MLC#3, Rack B Rack A	AO 가
9	98. 6	MLC#1, Rack A AI	AI 가
10	98. 12	BCS#2	
11	99. 1	BCS	2 가
12	00. 11	BCS (15 4)	()

4.

4.1

가

(ProcessSuite™)

BCS

[3].

(history data)

PC

가

2

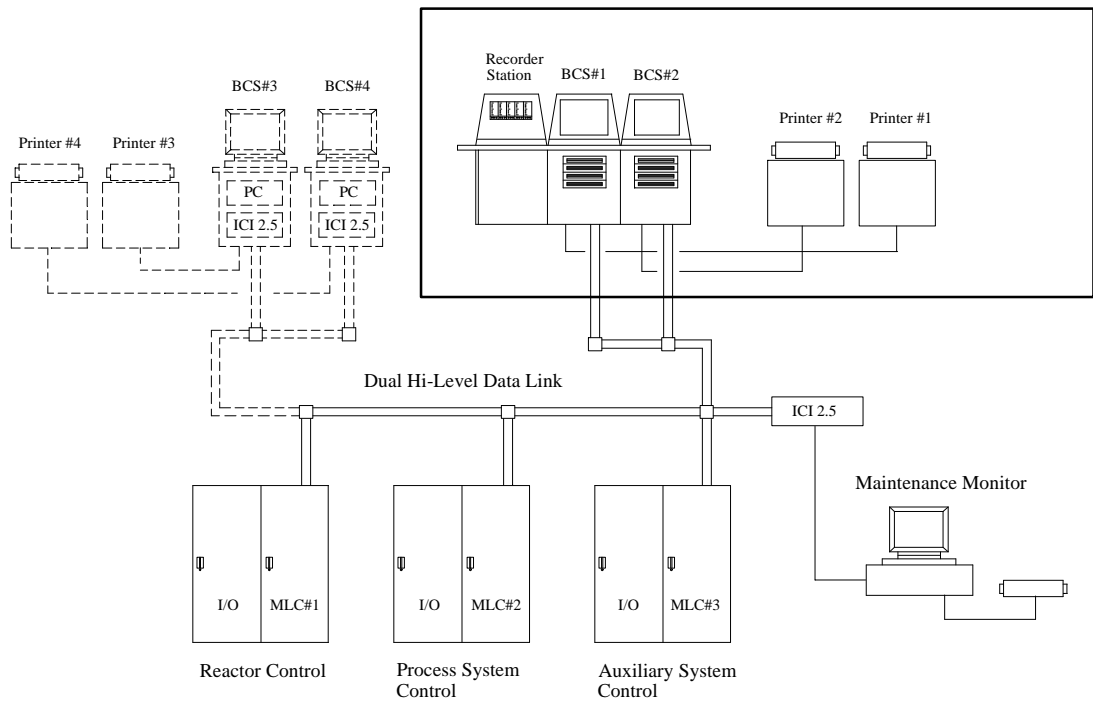
PC

2

CRT

(High Level Data Link)

2



2.

BCS

1

가

1

4.2

2000

“PC

PC

[4].

- 1.
- 2.
- 3.
- 4.
- 5.
- 6.

7.

programming language) 5.0 KMRRESIM[4]
MS Windows™ (object-oriented)
[5]. (graphic user interface)

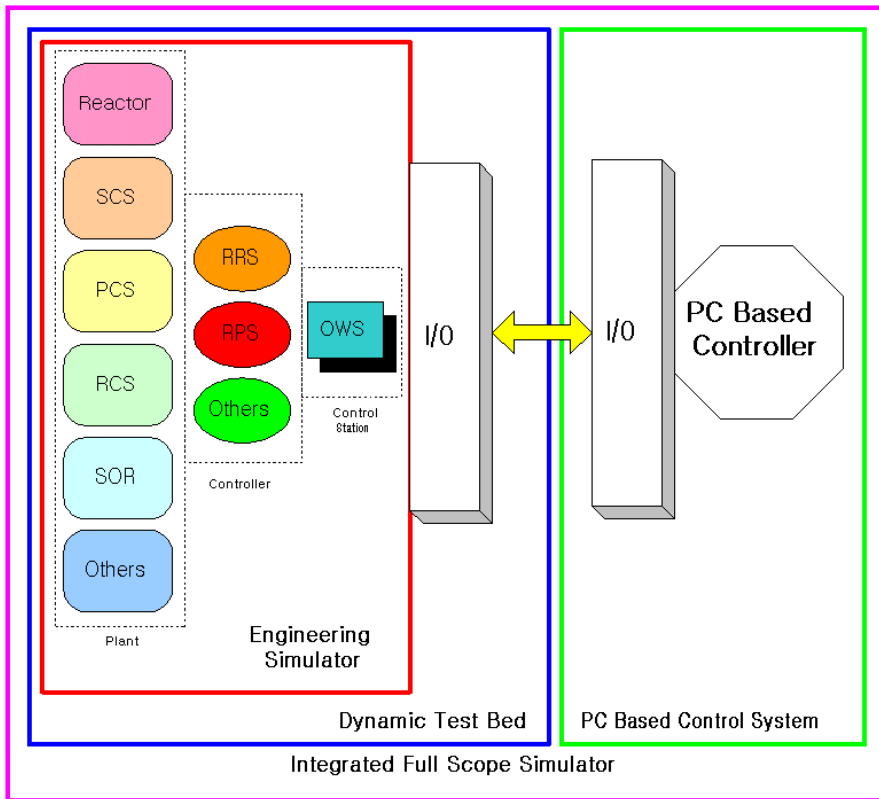
가

가

4.3.

(DTB: Dynamic Test Bed)

. DTB



3.

3

simulator)

(full scope

DTB

(engineering unit) 가 . DTB
 (I/O Interface)

. DTB

DTB 2 I/O

2.

	Analog Inputs		Analog Outputs	Contact Inputs	Contact Outputs	Discrete Outputs	TOTAL
	mA DC	RTD					
MLC#1	21	1	10	47	4	40	123
MLC#2	30	34	4	142	21		231
MLC#3	32	21	11	168	10		242
TOTAL	83	56	25	357	35		596

4.4 PC

PC

C++

. I/O 가 DTB가

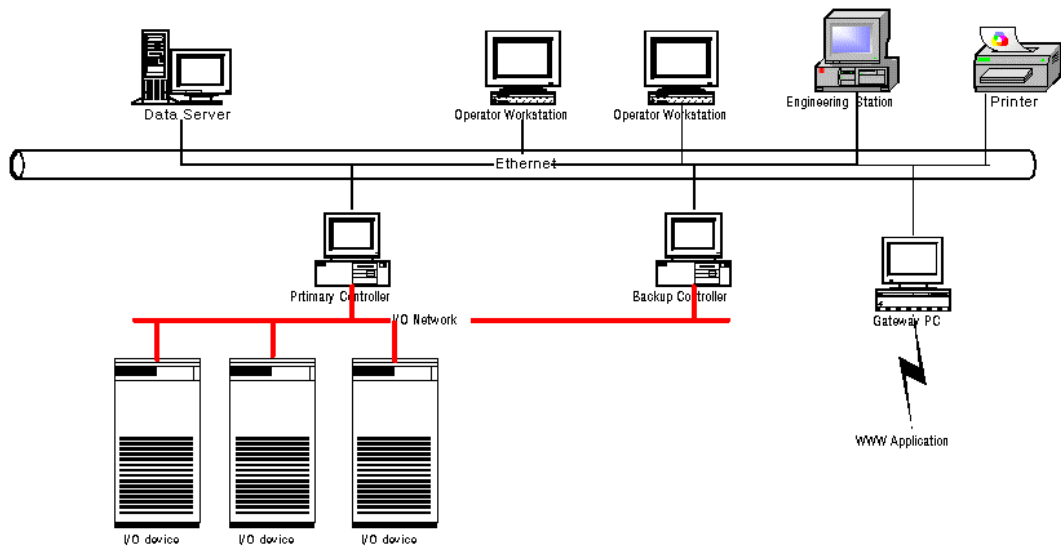
3

PC

가

가

4 PC



4. PC

5.

20

PC

Windows NT™

PC

1

1

2

가 DTB

PC

가

- [1] Y. K. Kim, “Application of Direct Digital Control System to Control and Operation of the HANARO Research Reactor” Presented at IAEA-TCM, Pitesti, Romania, Oct. 19 – 21, 1998
- [2] Design Manual, “Programmable Controller System”, KM-664-DM-001, 1990
- [3] M.J.Kim, “Technical Specification – PrecessSuite™ Vision Development Package for BCS Upgrade”, HAN-RS-DD-SP-00-003, KAERI, Sep.18, 2000
- [4] H. S. Jung et al. “Development of HANARO Engineering Simulator(I)” Research Report, KAERI/RR-2092/2000, KAERI, Jan. 2001.
- [5] H. S. Jung, P. H. Seong, “Development of A Highly Modularized Object Oriented Real Time Engineering Simulator for the HNARO Research Reactor, *Proceeding of Korean Nuclear Society, Autumn meeting, Taejon Korea, Oct. 2000.*